Docket No.: ELNX-0530

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App No.: Not yet assigned Filed: Herewith
Title: MANAGEMENT OF DATA BEFORE ZERO VOLT SUSPEND IN COMPUTER
POWER MANAGEMENT
Inventors: Seiler, et al.
Attorney: Joseph R. Condo Phone: (215) 568-3100
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Peripherals

1/8 BIOS ROUTINES $/^{32}$ -31 Power Management 28 Control Routines **BIOS CHIP** DRAM **System Board** ස 霳 Hard Disk Drive +3.3V Optional 20 +5V Optional 8 -5V, +12V, -12V KBC **KB** 22 53. 56 Fig. 1A 4 Fan +5V 8 1-30W 45P 1-30W 45P 3

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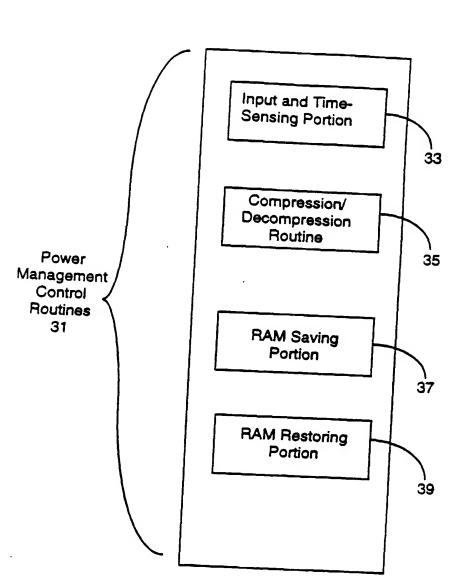


Fig. 1B

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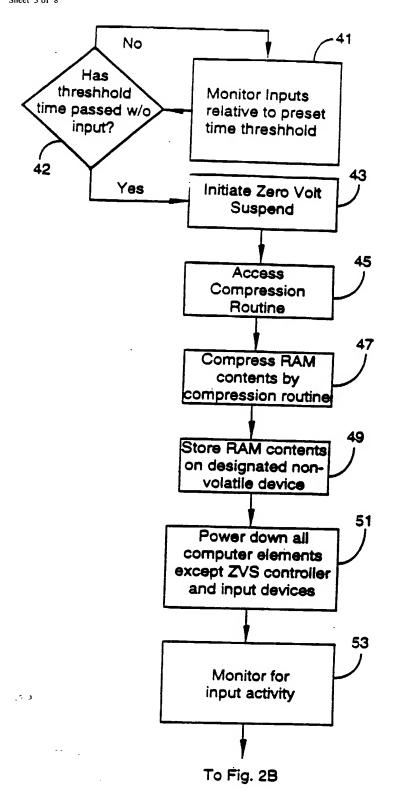


Fig. 2A

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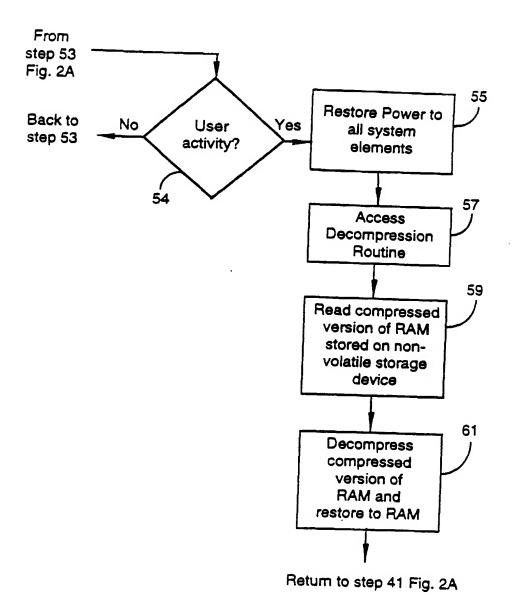


Fig. 2B

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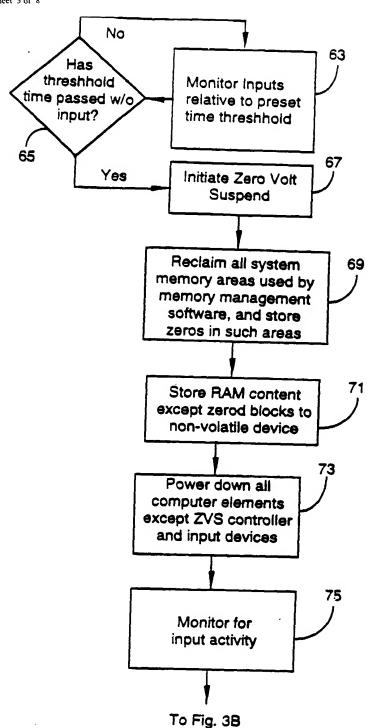


Fig. 3A

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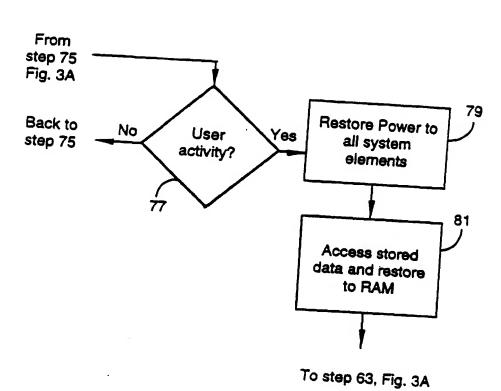


Fig.3B

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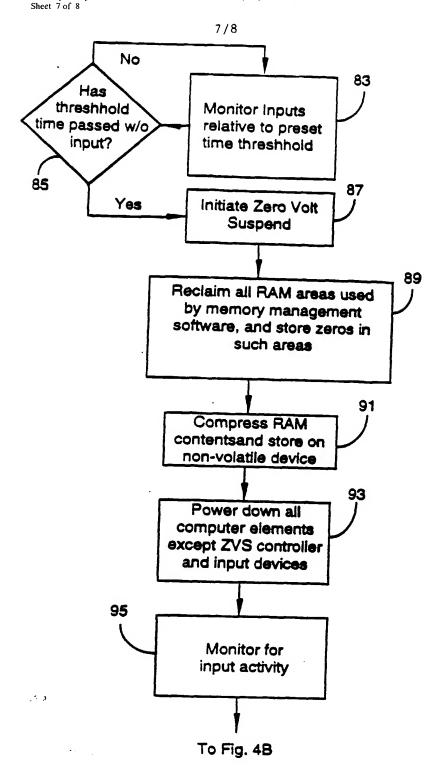


Fig. 4A

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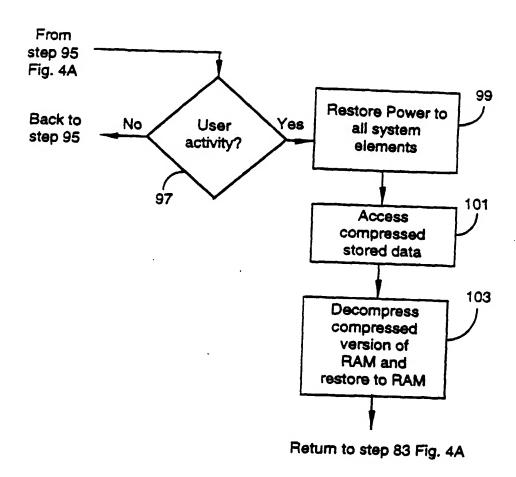


Fig.4B